

Curriculum Vitae

Pradeep Kumar Biswal, Ph.D.

S/O- Nanda Kishore Biswal

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Objective

I aim at achieving academic excellence and want to be a successful academician. My motto is to impart quality education in the field of computer science. I want to edify my career by nurturing young talents and contributing in true research. I expect a professional environment where I can be a benefactor and able to grow so as to exemplify my royal profession as a teacher. I want to earn a good name for my organization through honesty and fidelity.

Education

- **Ph.D (CSE):** From Indian Institute of Technology Guwahati, Assam, India in July, 2017.
- **Master of Technology (CSE):** From Indian Institute of Technology Guwahati, Assam, India in 2011, with CPI **8.00/10**.
- **Bachelor of Engineering (CSE):** From Dhaneshwar Rath Institute of Engineering and Management Studies, Cuttack (Biju Patnaik University of Technology (BPUT), Odisha) in 2004 with **74.14%** of marks.
- **10+2 science:** From Upendra Nath College, Balasore, Odisha in 1999 with **66.67%** of marks.
- **Class-10:** From Bhadrak High School, Bhadrak, Odisha in 1997 with **72.67%** of marks.

Achievements

- All India Rank 378 in Graduate Aptitude Test in Engineering (**GATE**) held in February-2009 with **99.09 percentile** .

Honorarium

- Served as Teaching Assistant for the NPTEL (Ministry of HRD, Govt. of India) Online Certification course "*VLSI Design Verification and Test*".

Area of research	<ul style="list-style-type: none"> ● VLSI testing. ● Computer Architecture. ● Asynchronous Circuit Design. ● Applications of Decision Diagrams
Subject taught	<ul style="list-style-type: none"> ● Computer Organization (Even semester for academic year 2017-18 at IIIT Bhagalpur) ● Design and Analysis of Algorithms (Odd semester for academic year 2018-19 at IIIT Bhagalpur) ● Operating System (Even semester for academic year 2018-19 at IIIT Bhagalpur) ● Computer Organization and Architecture (Odd semester for academic year 2019-20 at IIIT Bhagalpur) ● Object Oriented Programming using C++ (Odd semester for academic year 2019-20 at IIIT Bhagalpur)
Projects	<p>Ph.D Thesis</p> <ul style="list-style-type: none"> ● <i>Decision Diagrams based On-line testing of digital VLSI circuits</i>, under the supervision of Dr. Santosh Biswas, Department of CSE, IIT Guwahati, Assam. <p>M.Tech Thesis</p> <ul style="list-style-type: none"> ● <i>Enhance the performance of cache coherence problem in LCMP using multicast tree</i>, under the supervision of Dr. Aryabratta Sahu, Department of CSE, IIT Guwahati, Assam.
Publications	<p><u>Journals</u></p> <ol style="list-style-type: none"> 1. P. K. Biswal and S. Biswas, <i>A Binary Decision Diagram based On-line Testing of Digital VLSI Circuits for Feedback Bridging Faults</i>, Microelectronics Journal, Elsevier, Vol. 46(7), Pages: 598–616, 2015 . 2. P. K. Biswal, K. Mishra, S. Biswas and H. K. Kapoor, <i>A Discrete Event System Approach to Online Testing of Speed Independent Asynchronous Circuits</i>, VLSI Design, Hindawi, Vol. 2015, Pages: 1–16, 2015. 3. P.K. Biswal, H.P. Sambho, S. Biswas, <i>A Discrete Event System approach to On-line Testing of digital circuits with measurement limitation</i>, Engineering Science and Technology, an International Journal, Elsevier, Vol. 19(3), Pages: 1473–1487, 2016.

4. P. K. Biswal, S. Biswas, *On-Line Testing of digital VLSI circuits at Register Transfer Level using High Level Decision Diagrams*, Microelectronics Journal, Elsevier, Vol. 67, Pages: 88–100, 2017.
5. P. K. Biswal, S. Biswas, *A Binary Decision Diagram Approach On-line Testing of Asynchronous Circuits with Static and Dynamic C-elements.*, Journal of Electronic Testing, Springer (Accepted).

Conferences

1. Pradeep Kumar Biswal and Santosh Biswas, *Timed Discrete Event System Approach to Online Testing of Asynchronous Circuits*, 23rd IEEE Mediterranean Conference on Control and Automation, page 341 - 348, 2015, Spain.
2. Pradeep Kumar Biswal and Santosh Biswas, *A Binary Decision Diagram Approach to On-line Testing of Asynchronous Circuits*, 32nd International Conference on VLSI Design and 18th International Conference on Embedded Systems (VLSID), 2019, New Delhi, India. [*Nominated for best paper award*].

Work Experience

- Worked as an Assistant Professor in the School of Computer Engineering at KIIT University, Bhubaneswar, Odisha for the period of two months (from August 2017 to September 2017).
- I am working as an Assistant Professor in the department of Computer Science and Engineering at IIIT Bhagalpur, Bihar, since October 2017.

Academic and Administrative Responsibility

- Faculty In-charge of Department of CSE, IIIT Bhagalpur, since May 2019.
- Warden for Boy's Hostel No.1 at IIIT Bhagalpur, since August 2018.
- Warden for Girl's Hostel at IIIT Bhagalpur, August 2018 – October 2018.
- Faculty In-Charge of official transport arrangement at IIIT Bhagalpur, since February 2019.

References

- Dr. Santosh Biswas
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GEC Campus, Sejbahar, Raipur-492015, Chhattisgarh, India.
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- Dr. Aryabartta Sahu
Associate Professor, Department of CSE, IIT Guwahati, India.

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**Personal
Information**

My father, Sri Nanda Kishore Biswal is a retired teacher and my mother, Smt. Janakilata Biswal is a sincere housewife. We are devotee of Sri Sri Thakur Anukulchandra and follow His principles wholeheartedly. I always try to realize each and every step of my life and give first priority to the philosophy of "Being and Becoming". Finally, I love my life and accept it as it appears. My date of birth is 28.02.1982. Besides my mother tongue, Oriya, I am well versed in English and Hindi.

**Present
Address**

Pradeep Kumar Biswal
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**Permanent
Address**

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Odisha-756117

Declaration

I hereby declare that the information furnished above is true to best of my knowledge.

Date:
Place:

Signature: